

GaN基AC/DC电源高效高密方案探索

---元胞化技术

Exploring the Ceiling of Efficiency and Density of GaN Based AC/DC
Power Supply-Cellular Circuit Technology

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ZJU-PMIC

Institute of Power Electronics –Zhejiang University

1

Applications of High Density & High Efficiency Two stage AC/DC Conversions



PEV



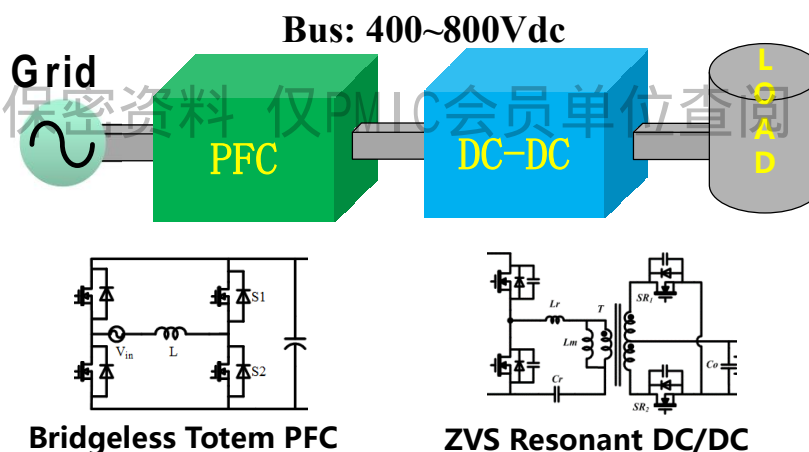
5G Base Station



IDC Racks



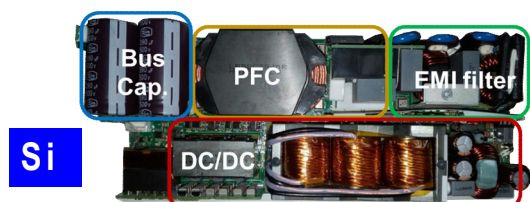
Full Electric
Aircrafts



2

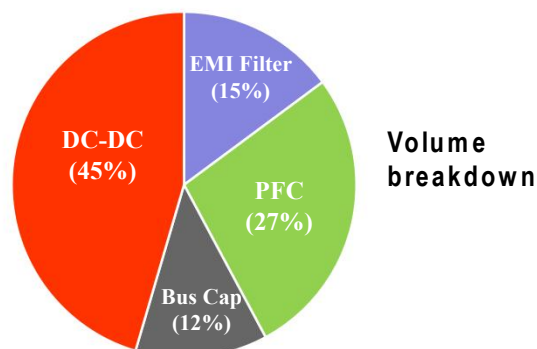
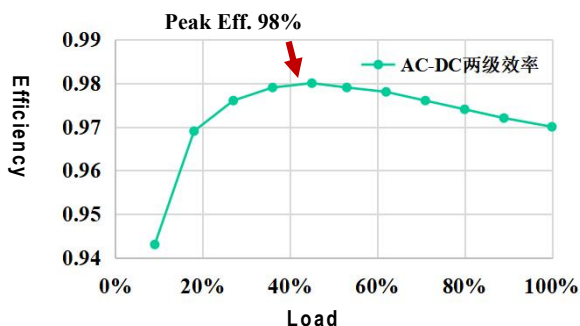
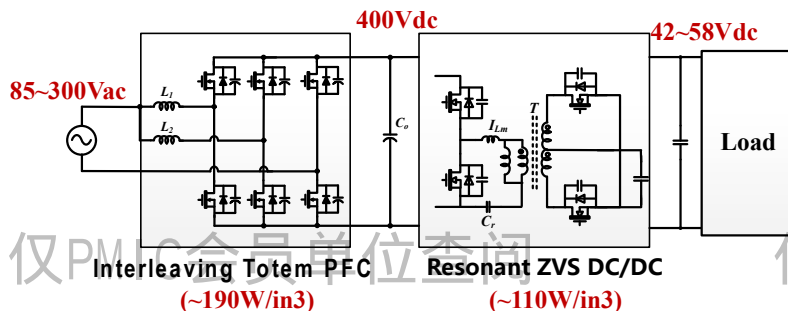
Power Density Limitation of Commercial AC/DC Server Power Supply with Si Devices

Commercial 3kW 48V AC/DC module



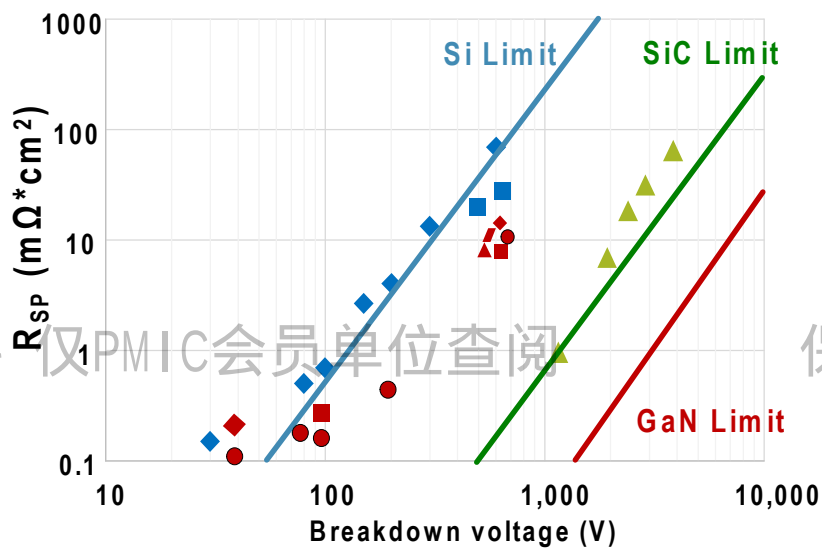
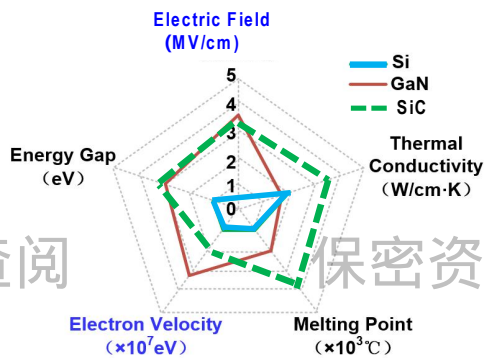
fs: <100kHz

功率密度 (两级) ~50W/in³



3

GaN has Larger Potential Improvements



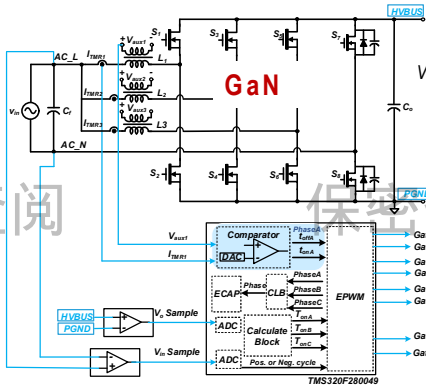
- ◆ Si MOSFET
- SJ Si
- ▲ SiC
- EPC
- ◆ I Company
- ▮ Transform
- Infineon
- Navitas
- ▲ Infineon

Ref: [1] N. Ikeda et.al. ISPSD 2008 p.289; [2] IR, APEC2012;

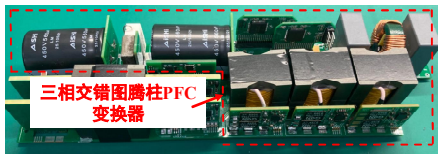
4

Density and Efficiency Compromise for Interleaving ZVS Totem-pole PFC

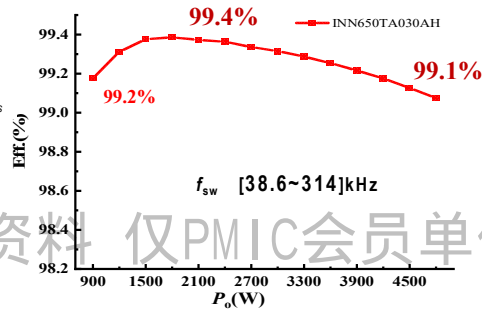
Control Strategy of Interleaving CRM Totem-pole PFC



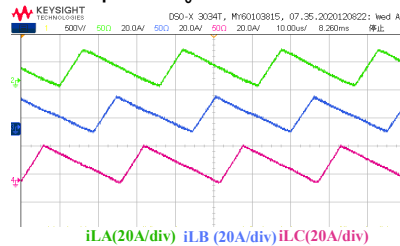
4.8kW/48Voutput ~190W/in³



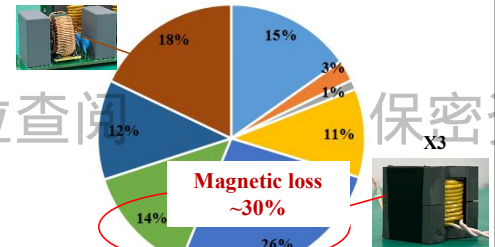
Measured Eff. (w/o auxiliary power)



Scope for P_o=4.8kW



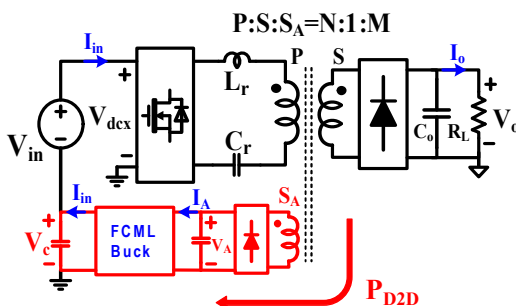
Loss Breakdown @4.8kW



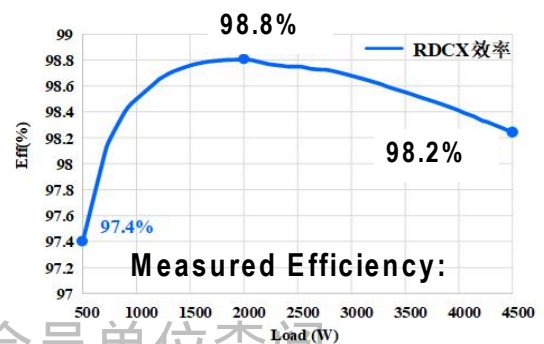
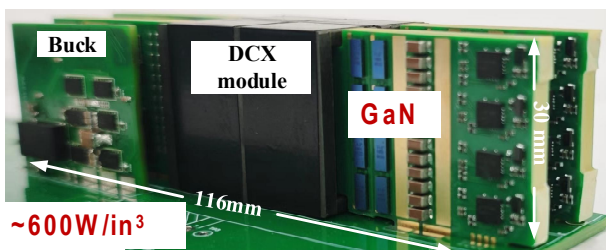
- GaN(高频管) 导通损耗
- GaN(高频管) 开关损耗
- GaN(高频管) 死区损耗
- Si(工频管) 导通损耗
- Si(工频管) 开关损耗
- Inductor 铜损
- Inductor 磁损
- 输出电容
- 滤波器

5

Efficient 2nd-Stage RDCX with all GaNs and Fixed Frequency Resonant DC/DC

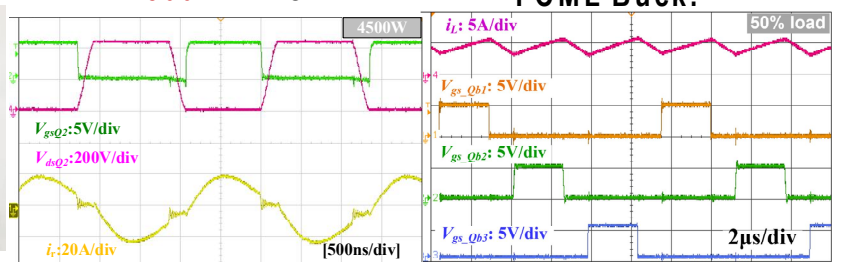


4.5kW DC-DC stage Prototype:



500kHz DCX

FCML Buck:

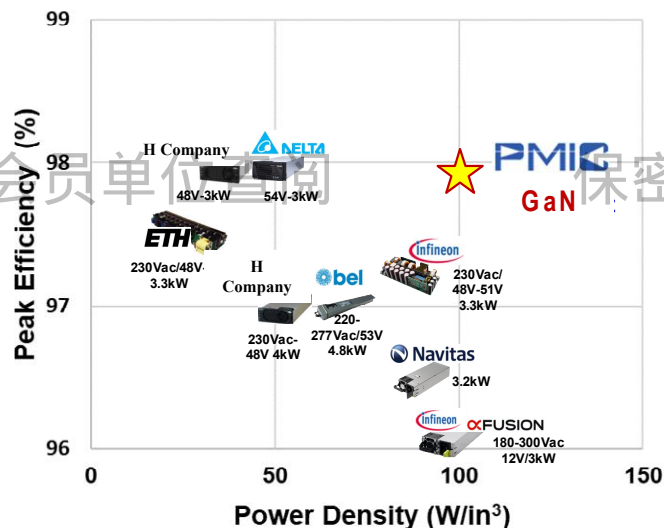
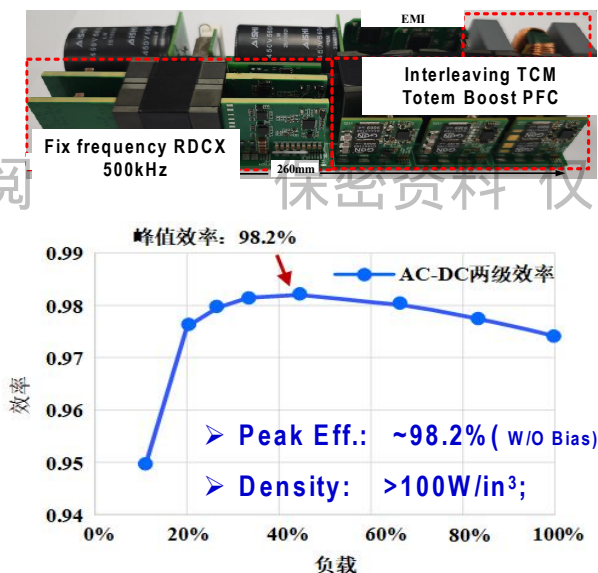


6

48V Efficient Server AC/DC Power Supply with 650V GaNs

Fig. of Prototype: **GaN** 4.5kW

State-of-the-Art:

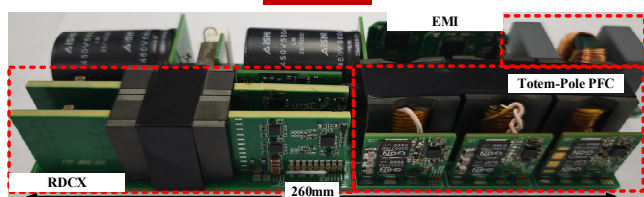


[2] LFC 3000E ENERGY, Delta
[3] M. Kasper, et al., Design of a Highly Efficient (97.7%) and Very Compact (2.2 kW/dm³) Isolated AC-DC. IEEE TPE, 2017
[4] PAC3000S12, Huawei

7

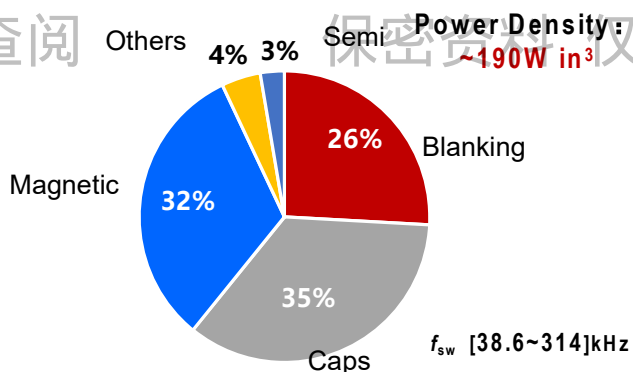
Magnetic Components Dominating the Volume of PFC and DC/DC

GaN 4.5kW

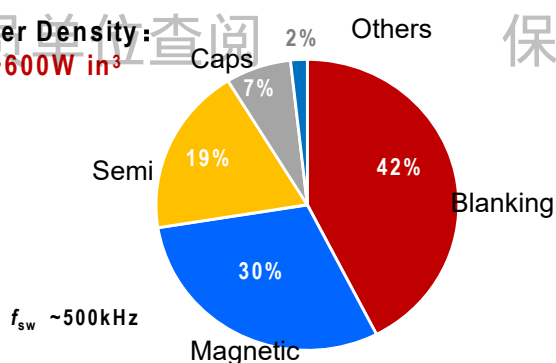


Two stage AC/DC
Power Density:
>100W in³

Totem PFC Volume breakdown

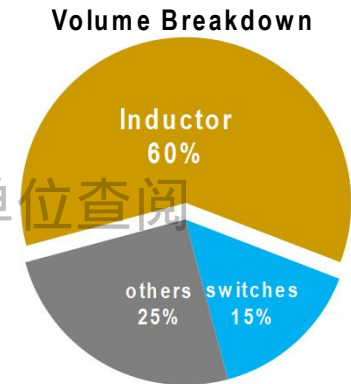
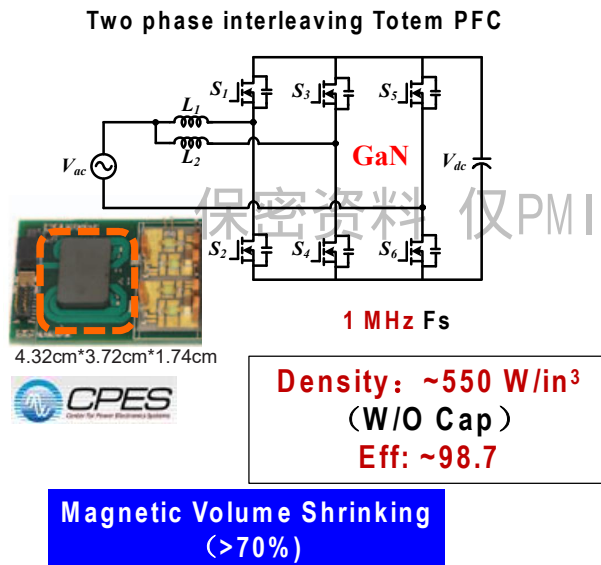


DC/DC Volume breakdown



8

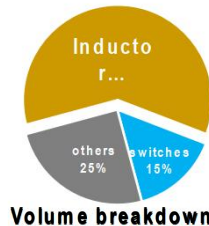
MHz Frequency Reduce the Inductance for Interleaving Totem Pole PFC with GaN Devices



Volume is still determined by Magnetic

F. C. Lee, "Application of GaN devices for 1 kW server power supply with integrated magnetics," in CPSS Transactions on Power Electronics and Applications, vol. 1, no. 1, pp. 3-12, Dec. 2016.

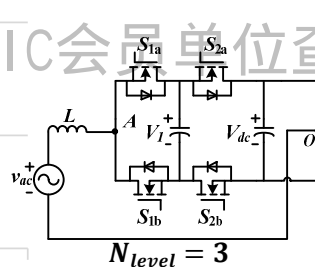
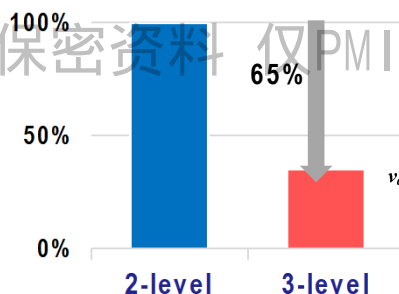
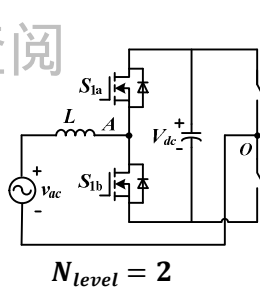
Multilevel Topology Reduce the Inductance Significantly



Inductance:
$$L = \frac{1}{f_s} \frac{V_{dc}}{(N_{level} - 1)^2} \frac{1}{4\Delta i_{L_max}}$$

Second Volt.

Volume
$$Volume \approx k_L \left(\frac{I_p I_{RMS}}{k_w J B_m} \right)^{3/4} L^{3/4}$$



Device Loss ?

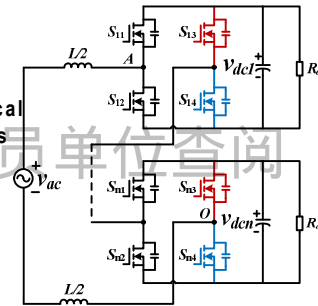
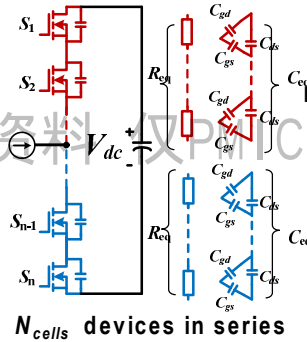
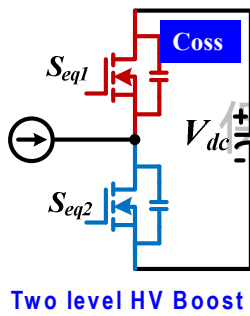


Boost Circuit

W. G. Hurley, "Transformers and inductors for power electronics :theory, design and applications,"
Y. Lei *et al.*, "A 2-kW Single-Phase Seven-Level Flying Capacitor Multilevel Inverter With an Active Energy Buffer," TPEL, Nov. 2017.
Y. Lei, W. Liu and R. C. N. Pilawa-Podgurski, "An Analytical Method to Evaluate ...," IEEE TPEL, March 2018.

Unified Cell Circuit Min. Loss Model with $NFoM_{oss}$ ($R_{ds_on} \times C_{oss}$)

$$P_{GaN_min} = 4V_{dc}I_{in}\sqrt{k_{temp}k_{dyn}(k_gI_{in}(1+k_{g2d})C_{gd(tr)}R_{ds_on} + C_{oss(er)}R_{ds_on})f_s}$$



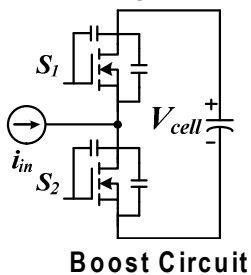
$$\begin{cases} NFoM_{sw} = C_{gd(tr)}R_{ds_on} \\ NFoM_{oss} = C_{oss(er)}R_{ds_on} \end{cases}$$

$$C_{eq} \times R_{eq} |_{C_{gs} \gg C_{gd}} = \frac{C_{oss(er)}}{N_{cells}} \times N_{cells} R_{ds_on} = C_{oss(er)} R_{ds_on} = NFoM_{oss}$$

Min. Loss depends on individual device's $NFoM_{oss}$

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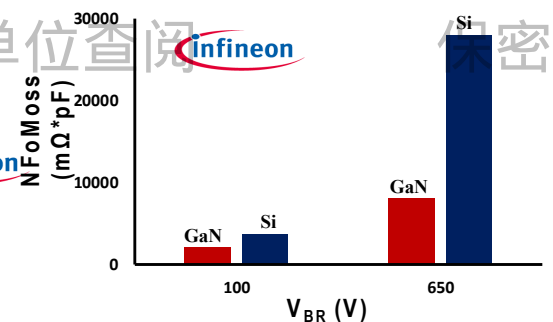
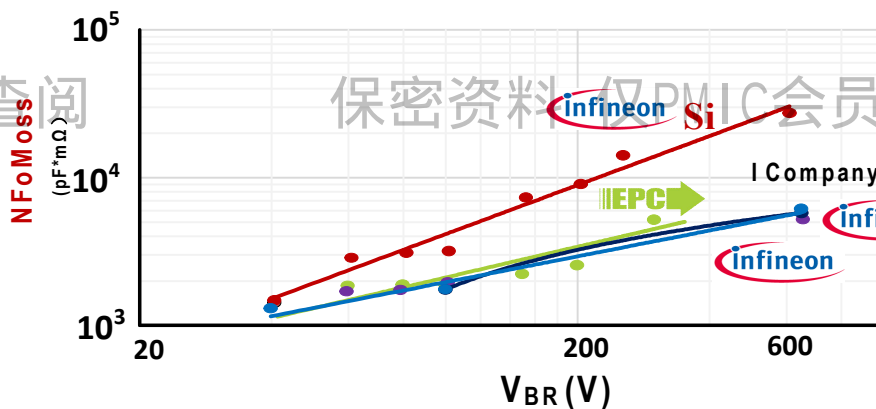
Multi-level Conerter with Low Voltage GaN has Much Room for Efficiency Improvement



$$P_{GaN_min} = 4V_{dc}I_{in}\sqrt{k_{temp}k_{dyn}(k_{sw}I_{in}NFoM_{sw} + NFoM_{oss})f_s}$$

Unified loss model

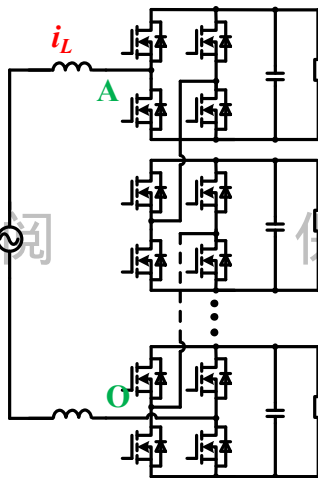
$$\text{Define: } \begin{cases} NFoM_{sw} = C_{gd(tr)}R_{ds_on} \\ NFoM_{oss} = C_{oss(er)}R_{ds_on} \end{cases}$$



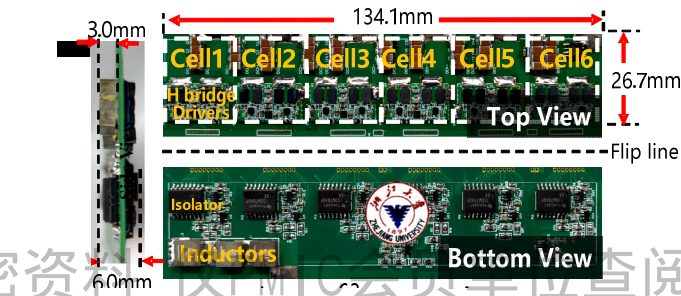
J Wu, X. Wu. "A unified switch loss model and design considerations for multilevel boost PFC with GaN devices" CPSS TPEA 2012

An Example: Cellular Circuit (CHB Multi-level) PFC Converter

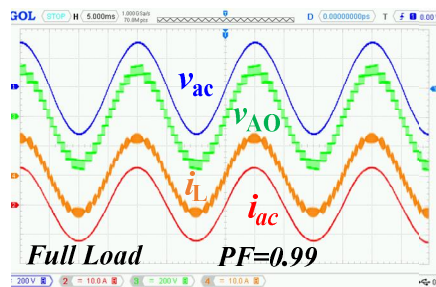
With commercial GaN



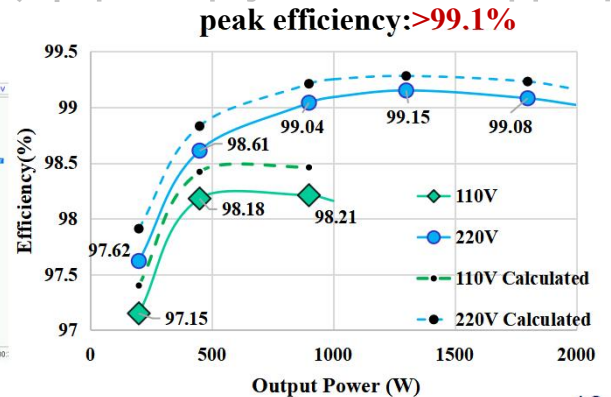
6-Cell CHB PFC



1.3 kW/in³@ 2kW
(w/o Bus Cap)



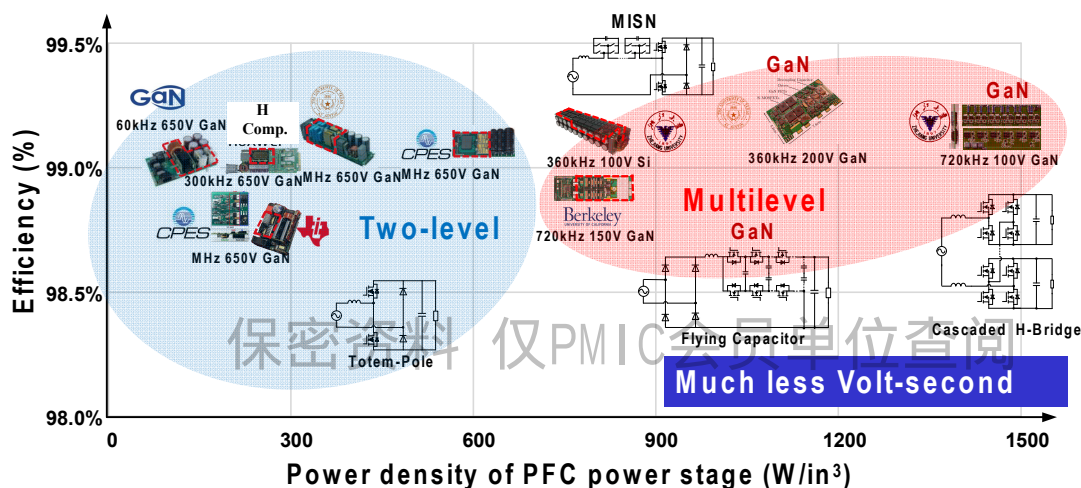
Full Load PF=0.99



J. Wu and X. Wu, "FoM Based Optimal Frequency and Voltage Level Design for High efficiency High density Multilevel PFC with GaN Device," 2020 APEC, pp. 1911-1915.

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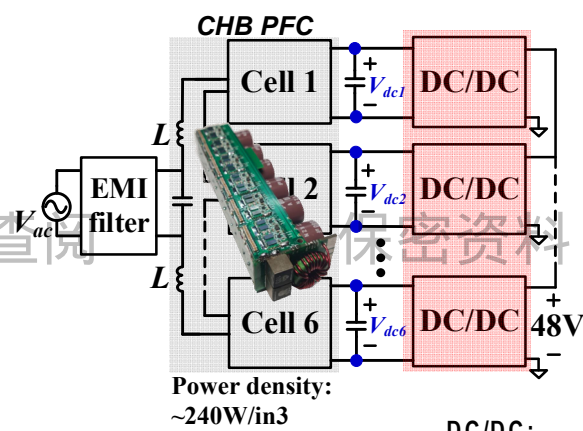
State-of-the-art High Power Density Single Phase PFC



- [1] GaN Systems, High Efficiency CCM Bridgeless Totem Pole PFC Design using GaN E-HEMT
- [2] Z. Liu, F. C. Lee, Q. Li, and Y. Yang, "Design of GaN-based Mhz totem-pole PFC rectifier," JESTPE2016.
- [3] <https://training.ti.com/1mhz-987-efficient-interleaved-16-kw-crm-gan-pfc>
- [4] A. Q. Huang, "Review of GaN totem-pole bridgeless PFC," STPEA2017
- [5] Lee, Fred C., et al. "Application of GaN devices for 1 kW server power supply with integrated magnetics," TPEL2016
- [6] D. Chou, R. C. N. Pilawa-Podgurski, "An Interleaved 6-Level GaN Bidirectional Converter for Level II Electric Vehicle Charging," APEC 2019
- [7] Q. Huang, Q. Ma, P. Liu, A. Q. Huang and M. A. de Rooij, "99% Efficient 2.5-kW Four-Level Flying Capacitor Multilevel GaN Totem-Pole PFC," JESTPE2021
- [8] J. Wu, et al, "FoM Based Optimal Frequency and Voltage Level Design for High efficiency High density Multilevel PFC with GaN Device," APEC2020
- [9] <https://www.clearesult.com/80plus/node/12300>
- [10] T. Huang, et al, "Single Phase High Power Density MISN PFC Converter," ECCE 2022

14

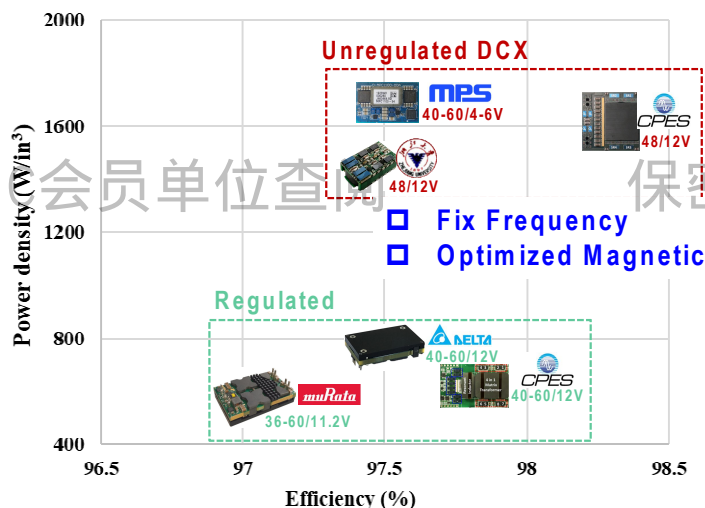
CHB Cellular PFC Needs Multiple Isolated DC-DC stages



- >60V-level input
- >Tight regulated and isolation
- >High efficiency and high density

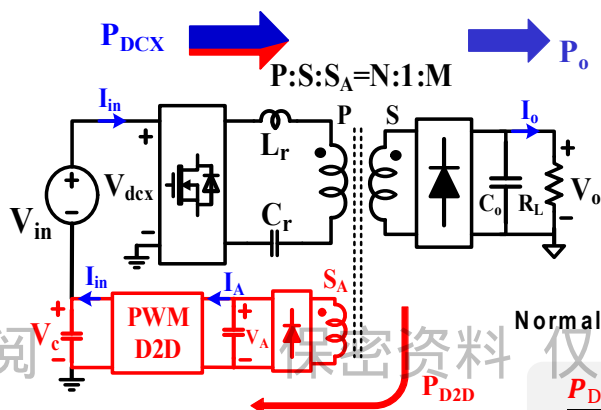
[1] DRQ-11.4/88-L48NB-C, MuRata
 [2] Q54SJ108A2, Delta
 [3] MPC1100-54, MPS
 [4] M. H. Ahmed, et al., High-efficiency, High-density Isolated/Regulated 48V Bus Converter... APEC 2019.
 [5] G. Li, et al. High Power Density 48-12 V DCX With 3-D PCB Winding Transformer. IEEE TPE, 2020
 [6] M. H. Ahmed, et al., Two-Stage 48V VRM With Intermediate Bus Voltage Optimization... IEEE JESTPE 2021.

State of the art of 48V-level isolated DC/DC:



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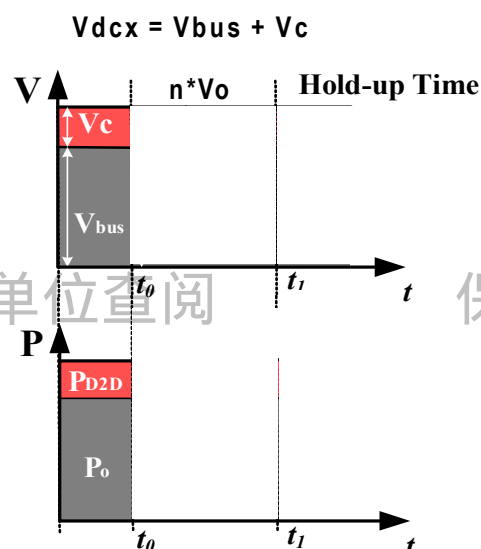
Regulating Principle of Single Transformer RDCX for Cellular D2D stage



Normal Power distribution:

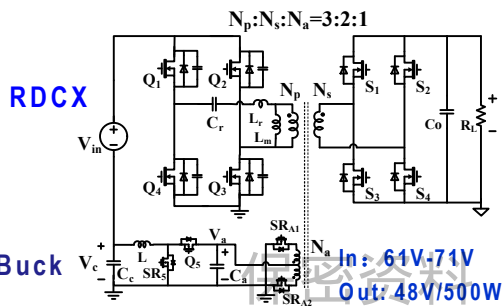
$$\frac{P_{D2D}}{P_o} = \frac{V_c}{V_{bus}} \approx 8\%$$

- Fixed frequency resonant DCX;
- Aux. Multi-level Buck for regulation;
- Low P_{D2D} @ normal operation;



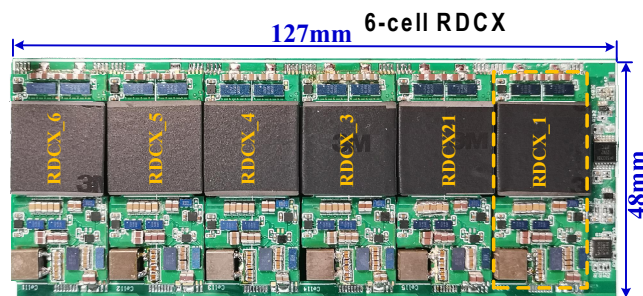
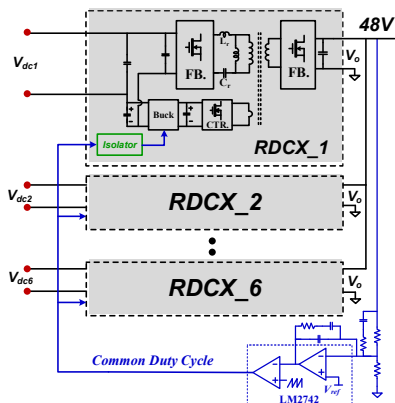
16

Common Duty-cycle Control for Cellular D2D Stage



Size: 43.5*19.5*6.5mm³

Density: ~1.5 kW/in³
Eff.: 98.1%

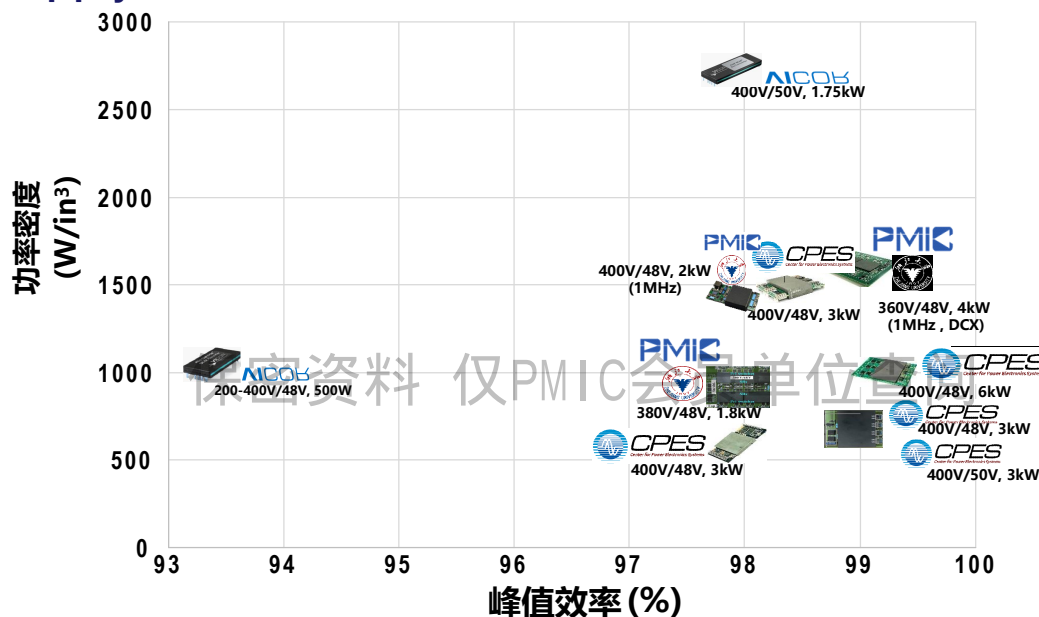


Power: 3kW
Density: ~1300W/in³
(with Controller)

Common Duty-cycle Control

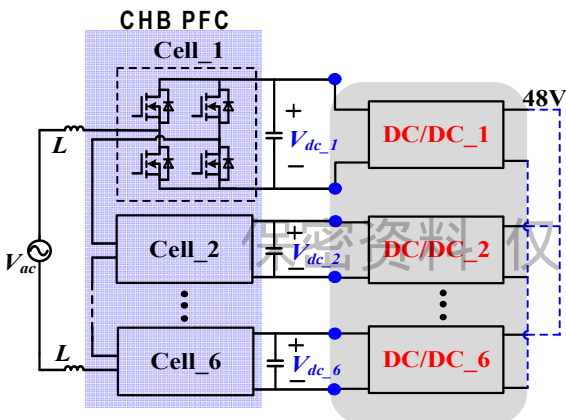
17

State-of-the-art of 2nd Stage DC/DC for 48V Server Power Supply

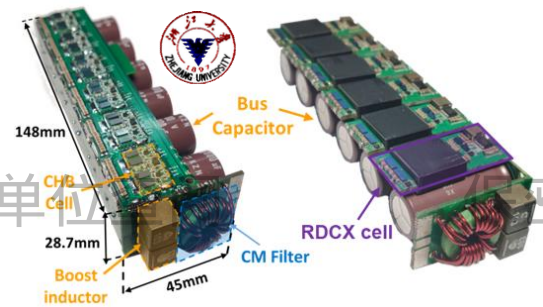


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Cellular Two-stage AC/DC Converter with Higher Power Density

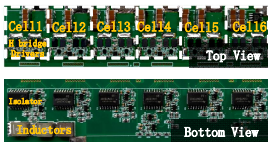


Two stages

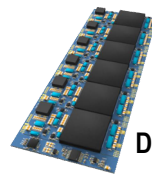


Density: **~180 W/in³** (including E-Cap)
Peak Eff.: **>97%**

Input: 90-290Vac
Output: 48 V/ 3kW



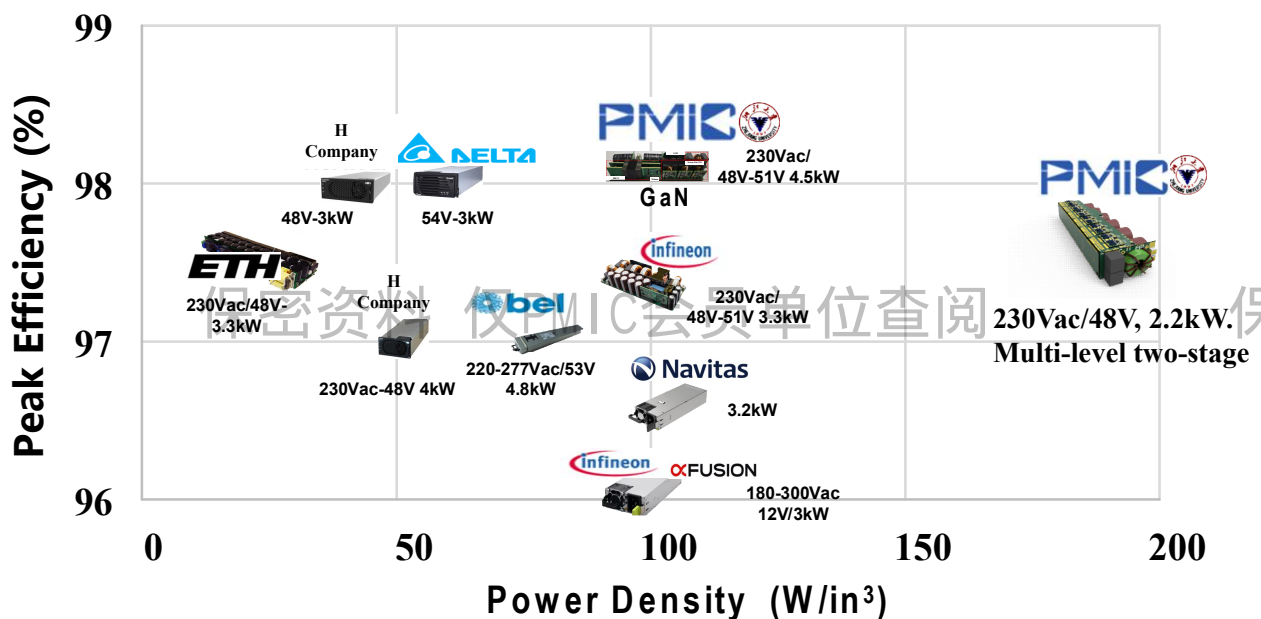
PFC: **~1.3kW/in³**
Eff.: **>99%** (@ 220Vac)



Dc/dc Density: **~1.5 kW/in³**
Eff. (@350W): **>98% @1MHz**

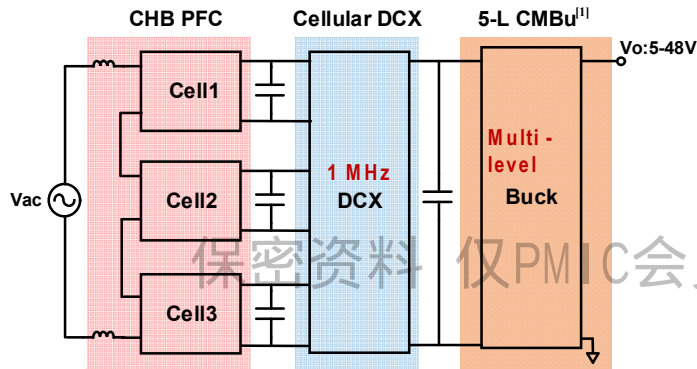
19

48V Efficient Cellular AC/DC Server Power Supply with All GaNs



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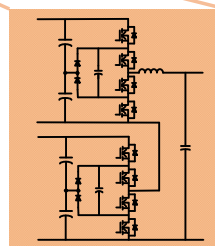
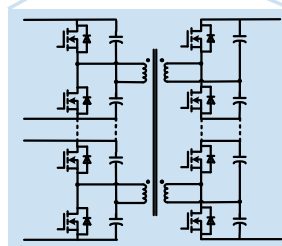
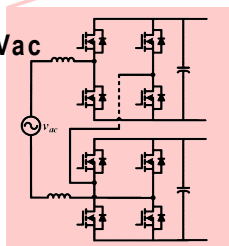
Cellular Circuit Topology CCT Achieving Ultra-High Efficiency and Power Density for 240W PD Power Supply



Features:

- CCM PFC improve efficiency @90Vac
- 1 MHz-DCX achieves high efficiency
- High efficiency among wide output range
 - Voltage range 5V to 48V(PD3.1)
- High power density & efficiency

90V~240Vac



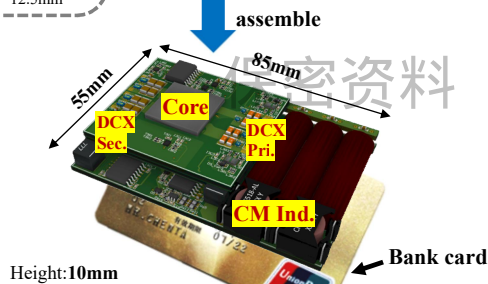
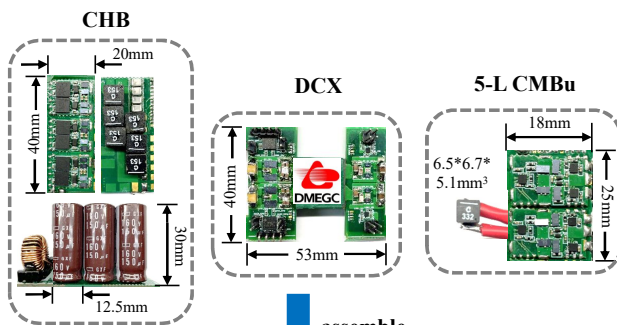
5V to 48V/5A

[1] Cascaded Multi-level Buck (CMBu)

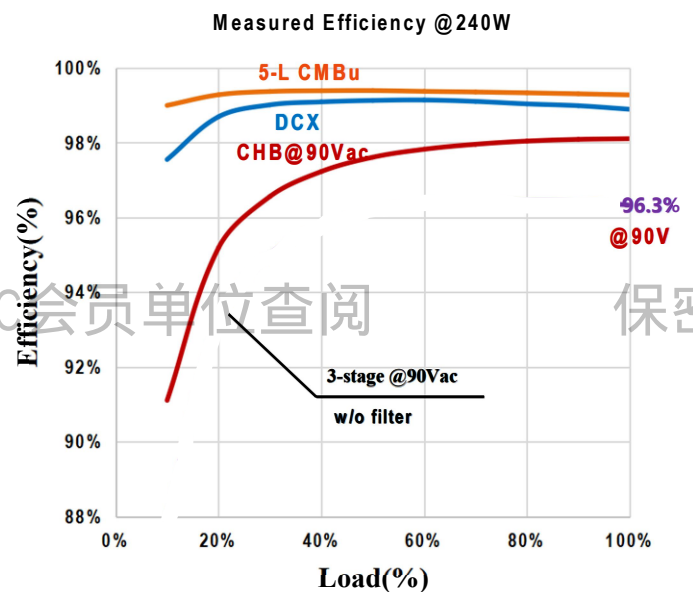
[2] Detailed design based on NFoM loss model refers to PMIC presentation in 2022.04

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Full-load Efficiency Achieves 96.3% @90Vac

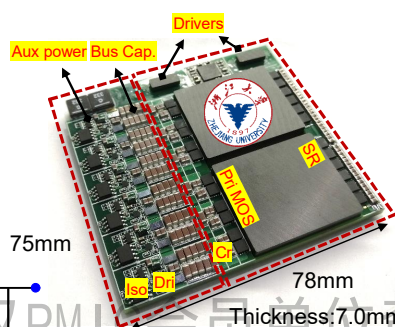
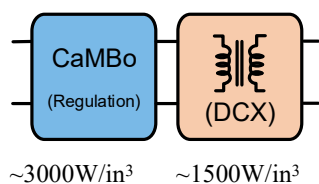


Acknowledgement:

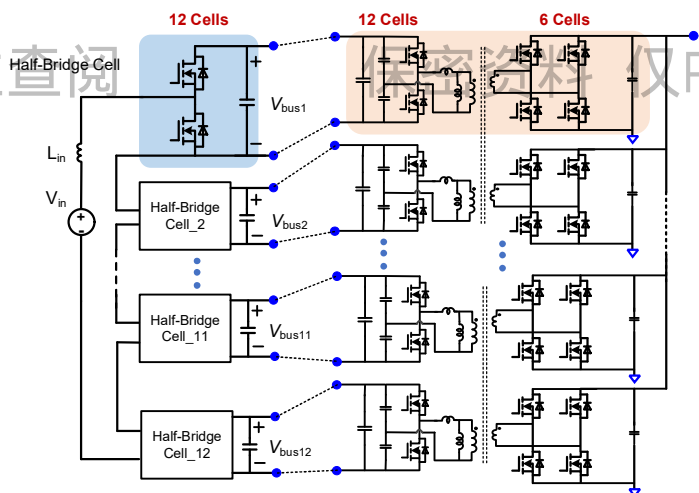


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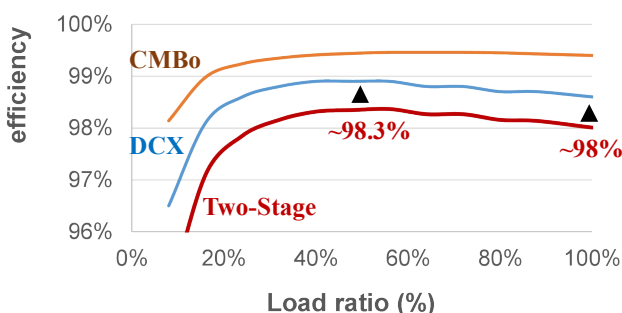
CCT Applied in Wide Voltage Range General Brick DC/DC Module



Vin: 200-400V
Vo: 17V/120A-34V/60A
Rated Power: 2.5kW
Efficiency @full load 28V: ~98%
Power density: > 1.2 kW/in³



➤ Measured Eff. @ 270Vin/28Vo (Rated)



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ZJU Power Management Innovation Consortium (ZJU-PMIC)



装置与系统公司 (26)

英飞源技术 INFY POWER	oppo	DELTA 台达	阳光电源 SUNGROW	杰瑞
AOHAI 奥海科技	FOX ESS 麦田能源	mi	航嘉 Huntkey	ZTE中兴
Great Wall 长城电源	MEGMEET	HAWUN 皓文电子	CukTech	Anker Innovations
LUXSHARETECH 立讯技术	ZM	Sevenstar 七星微电子	爱科赛博 ACTIONPOWER	JDENERGY 奇点能源
SHINRY 欣锐科技	EVTECH 富特科技	晟星和·SXH	BOCO 铂科电子	DA FENG

元器件公司 (9)

infineon	SILERGY
DMEGC	铂科新材
Mentech 铭普光磁	IWJOUWATT
SOUTHCHIP	SINOMAGS 希磁科技
Si-power	

Members 2024

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ZJU Power Management Innovation Consortium
(ZJU-PMIC)



PMIC

高校主导、行业共建的电源管理技术生态链

器件

装置

系统

矽力杰半导体
杰华特半导体
铭普光磁
硅动力
横店东磁
希磁科技
英飞凌科技
南芯半导体
铂科新材

深圳英飞源	阳光电源
杰瑞电子	奥海科技
长城电源	麦田能源
浙江富特科技	酷科电子
铂科电子	航嘉
皓文电子	振华微
麦格米特	欣锐科技
晟星和	爱科赛博
七星华创	聚新汽车
立讯技术	

QPPO技术
台达电子
中兴通讯
小米科技
安克创新
奇点能源
蔚斯博系统

国内首个

合作生态

模式探索

未来技术

<https://PMIC.ZJU.EDU.CN>

Thank You
for Your Time

